

# Low Partial Pressure Chemical Vapor Deposition of Graphene on Copper

Jie Sun, Niclas Lindvall, Matthew T. Cole, Koh T. T. Angel, Teng Wang, *Student Member, IEEE*, Kenneth B. K. Teo, *Member, IEEE*, Daniel H. C. Chua, Johan Liu, *Fellow, IEEE*, and August Yurgens

**Abstract**—A systematic study of the Cu-catalyzed chemical vapor deposition of graphene under extremely low partial pressure is carried out. A carbon precursor supply of just  $P_{\text{CH}_4} \sim 0.009$  mbar during the deposition favors the formation of large-area uniform monolayer graphene verified by Raman spectra. A diluted  $\text{HNO}_3$  solution is used to remove Cu before transferring graphene onto  $\text{SiO}_2/\text{Si}$  substrates or carbon grids. The graphene can be made suspended over a  $\sim 12$   $\mu\text{m}$  distance, indicating its good mechanical properties. Electron transport measurements show the graphene sheet resistance of  $\sim 0.6$   $\text{k}\Omega/\square$  at zero gate voltage. The mobilities of electrons and holes are  $\sim 1800$   $\text{cm}^2/\text{Vs}$  at 4.2 K and  $\sim 1200$   $\text{cm}^2/\text{Vs}$  at room temperature.

**Index Terms**—Chemical vapor deposition, graphene, low partial pressure, nanoelectronics, wet transfer.

## I. INTRODUCTION

GRAPHENE is an atomically thin network of carbon atoms arranged in a hexagonal lattice; it is being intensively studied because of its fascinating electrical and optical properties [1]. Graphene electronic band structure can be engineered by making nanoribbons of various widths [2]. By exploiting the ambipolar transport properties of graphene, full-wave rectification and frequency doubling can be realized at high frequency [3].

Manuscript received April 3, 2011; accepted June 20, 2011. Date of publication July 29, 2011; date of current version March 9, 2012. This research work was supported by the Swedish Research Council and the Swedish Foundation for Strategic Research. The clean-room processing involved the equipment sponsored by the Knut and Alice Wallenberg Foundation. The work of M. T. Cole was supported in part by the Isaac Newton Trust and St Edmund's College Cambridge. The work of K. K. T. Angel and D. H. C. Chua was supported in part by the National University of Singapore under Grant WBS R284-000-087-112. The work of T. Wang and J. Liu was supported in part by the Swedish National Science Foundation under Contract No. 2009-5042 SSF Provoking program as well as the Chalmers area of advances: Production. The review of this paper was arranged by Associate Editor C. Zhou.

J. Sun, N. Lindvall, and A. Yurgens are with Quantum Device Physics Laboratory, Department of Microtechnology and Nanoscience, Chalmers University of Technology, SE-41296 Gothenburg, Sweden (e-mail: jiesun@chalmers.se; niclas.lindvall@chalmers.se).

M. T. Cole is with the Electronic Devices and Materials Group, Electrical Engineering Division, Engineering Department, University of Cambridge, CB3 0FA Cambridge, U.K. (e-mail: mtc35@cam.ac.uk).

K. T. T. Angel and D. H. C. Chua are with the Department of Materials Science and Engineering, National University of Singapore, 117576 Singapore (e-mail: angel.koh@nus.edu.sg; danielchua@nus.edu.sg).

T. Wang and J. Liu are with BioNano Systems Laboratory, Department of Microtechnology and Nanoscience, Chalmers University of Technology, SE-41296 Gothenburg, Sweden (e-mail: teng.wang@chalmers.se; jliu@chalmers.se).

K. B. K. Teo is with AIXTRON, Anderson Road, Swavesey, Cambridge, CB24 4FQ Cambridge, U.K. (e-mail: k.teo@aixtron.com).

Color versions of one or more of the figures in this paper are available online at <http://ieeexplore.ieee.org>.

Digital Object Identifier 10.1109/TNANO.2011.2160729

Graphene has a high charge-carrier mobility that is promising for making ultrafast transistors. Graphene is believed to play an important role in future nanoelectronics [4].

Mechanical exfoliation of graphite is the traditional technique to prepare graphene [5]. However, the method is not scalable and the resulting graphene flakes are small. Nowadays, there are two main strategies for fabricating high-quality graphene in large scale. The first is the high-temperature decomposition of silicon carbide where Si is evaporated leaving behind free carbon atoms that subsequently form graphene layers at the surface [6]. However, this method is expensive and is not suitable for graphene transfer onto other substrates. On the other hand, catalytic chemical vapor deposition (CVD) of graphene on the surfaces of several transition metals [7], [8] is a scalable technique which is compatible with the existing semiconductor-industry processing. Recently, 30-inch graphene films for making transparent electrodes have been realized by CVD [9], making the technique very promising for future applications.

Ni [7] or Cu [8] are typical catalysts used in CVD of graphene. However, the growth mechanisms associated with each catalyst are different. In the former case, carbon dissolves in Ni at high temperature and then precipitates forming graphene before and during the cool down. The method is sensitive to process timing that is not optimal for getting a monolayer of graphene in every sample. In contrast, the solubility of C in Cu is small; the formation of graphene largely results from the surface chemistry limiting the number of graphene layers to a minimum. The Cu-catalyzed graphene CVD is, however, subject to surface kinetics [10] that allows for multilayer graphene as well, depending on the processing parameters [10]–[13].

In this paper, we provide a summary of our recipe of large-area graphene produced on Cu by CVD. One of the unique features of our experiments is that graphene is grown under one to four orders of magnitude lower partial pressure of hydrocarbon than reported earlier in the literature. The low carbon concentration 1) lowers the deposition rate that 2) decreases the density of nucleation centers [14], [15] promoting large graphene domains, and 3) suppresses formation of multilayer graphene. Although carbon does not have high chemical affinity to Cu [16], our results suggest that it is possible to achieve high-quality graphene using even extremely small amount of carbon atoms in the precursor gas. This work can help a better understanding of the CVD of graphene, important for its future large-scale production.

## II. MATERIAL GROWTH

The graphene is produced in a cold-wall low-pressure CVD system specially designed for carbon nanomaterial deposition

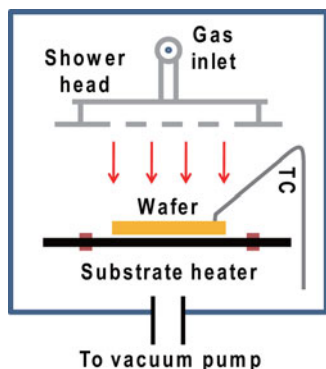


Fig. 1. Schematic of the cold-wall CVD system used in this study. The specimen is placed on a current-heated graphite holder situated at about 10 cm below the gas inlet shower head.

(Black Magic, AIXTRON Nanoinstruments Ltd.), as illustrated in Fig. 1. The samples are directly put onto a low-mass Joule heater which is capable of rapid thermal ramp rates. The temperature is measured by a thermocouple (TC) contacting the sample. A quartz shower head is used to premix and introduce the gas mixture uniformly across the substrate surface. The catalysts used in this study are 25- $\mu\text{m}$ -thick Cu foils (99.9%, GoodFellow) and 300 nm Cu thin films evaporated on thermally oxidized silicon wafers (400 nm  $\text{SiO}_2$ ). The Cu thin film thickness is crucial for the high-temperature graphene growth. If the film is too thin (e.g., 100 nm), Cu partially evaporates/sublimates at high temperature leading to a holey structure [17]. On the other hand, too thick a film (e.g., 2  $\mu\text{m}$ ) accumulates large strain due to the variation in thermal expansion coefficients between the Cu and the substrate, resulting in film delamination. 200–400 nm is the optimal thickness in our experiments.

The main precursor gas is a high purity methane (99.9995%) prediluted with Ar to 5%. While methane generally gives the best graphene quality, other hydrocarbons such as acetylene or ethylene can be used as well. The advantage of  $\text{CH}_4$  is that it has only one C atom per molecule and a high decomposition temperature [18]. The samples are first cleaned in acetic acid, acetone, and isopropanol to remove the native oxides and organic contaminations. Then, they are heated to 1000  $^\circ\text{C}$  at 300  $^\circ\text{C}/\text{min}$  and annealed for 5 min, under a flow of 20 sccm  $\text{H}_2$  and 1000 sccm Ar. Subsequently, 30 sccm prediluted  $\text{CH}_4$  is introduced into the chamber to initiate graphene growth. The partial pressure  $P_{\text{CH}_4}$  is  $9 \times 10^{-3}$  mbar at the total pressure  $P$  of 6.35 mbar (Fig. 2). Our  $P_{\text{CH}_4}$  value is one to four orders of magnitude lower than used elsewhere [19]–[21], in spite of some groups reporting that the graphene growth under such an extreme condition is impossible [22]. This partial pressure corresponds to  $\sim 150$  graphene layers grown per second assuming the 100% efficiency of  $\text{CH}_4$  decomposition and C sticking to the surface. After 5 min of deposition, the three gasses are stopped and the system is evacuated to  $< 0.1$  mbar. Finally, the samples are cooled at 300  $^\circ\text{C}/\text{min}$  to room temperature under 20 sccm  $\text{H}_2$  and 1000 sccm Ar.

Fig. 3 shows optical images of the Cu foil (a) and thin film (b), respectively. No apparent changes in the morphology are observed after the graphene growth except that the grain bound-

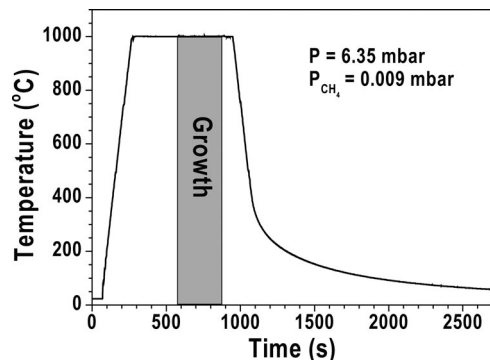


Fig. 2. Temperature profile of a typical low partial pressure graphene deposition process. Very fast (300  $^\circ\text{C}/\text{min}$ ) heating and cooling ramp rate is used. The carbon precursor is evacuated immediately after the growth, preventing any possible deposition during chamber cooling.

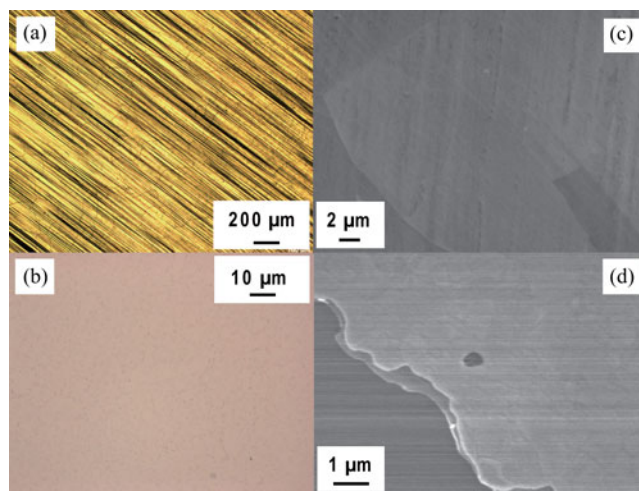


Fig. 3. Optical and SEM images showing the morphology of the Cu foils (a) and (c) and Cu thin films (b) and (d) after the graphene CVD. The sample shown in (d) is grown by low partial pressure CVD with  $\text{C}_2\text{H}_2$  precursor to enhance the observed graphene overgrowth.

aries of the poly-crystalline Cu become visible. The grains in the Cu foil are up to several 100  $\mu\text{m}$  large to be compared to much smaller grains in the Cu thin film (a few  $\mu\text{m}$ ). The bigger grain size can account for the improved quality of the graphene produced on the Cu foil than on the Cu thin film. Fig. 3(c) and (d) is scanning electron microscopy (SEM) images of the Cu foil and thin-film samples, respectively. The grains are more clearly visible, but no graphene wrinkles or islands are observed [8], indicating continuous graphene sheets conformally attached to the copper surfaces. The Cu thin film has a weak adhesion to the  $\text{SiO}_2/\text{Si}$  substrate and can therefore be detached by simply using a sticky tape. Fig. 3(d) is taken at the wafer edge, where some parts of the Cu thin film are intentionally peeled off prior to the graphene growth. Interestingly, overgrowth of the graphene can be directly observed. The graphene is sticking out several hundred nm from the Cu thin film edge. This implies that graphene can smoothly grow over Cu grain boundaries to form a continuous film. By properly designing the geometry of the holes or trenches, suspended graphene for microelectromechanical

system (MEMS) applications should be possible to realize directly on prepatterned Cu thin films.

### III. FILM TRANSFER

The as-prepared graphene can be transferred to other substrates by a wet-etching technique. We use spin-coated copolymer MMA(8.5)MAA (EL10) to mechanically support graphene after copper is etched away. Graphene grows also at the bottom side of the Cu foil because the gases easily penetrate the small gap between the heater and the foil. This graphene layer is removed by oxygen plasma while the top-side graphene is protected by the copolymer. For the Cu thin film samples on SiO<sub>2</sub>/Si, the plasma etching is not needed, but it is necessary to scratch the sample edges to remove the resist to ease the separation of the copolymer from the substrate following the Cu etching. FeCl<sub>3</sub>·6H<sub>2</sub>O or Fe(NO<sub>3</sub>)<sub>3</sub>·9H<sub>2</sub>O can be used as etchants. However, we find that some impurities (possibly Fe) in commercial Cu foils cannot be completely etched leaving contaminating residues at the graphene surface. Nitric acid etches almost all metals with few exceptions like Au and Ta. Therefore, we use HNO<sub>3</sub> to etch Cu to obtain clean graphene sheets without extra cation contaminations. We dilute available 70% HNO<sub>3</sub> by H<sub>2</sub>O (1:7) to minimize possible oxidation of graphene. The etching time is typically several hours. The graphene/copolymer samples are then rinsed in water and aligned to the target substrates. As the copolymer is only a few hundred nm thick, adhesive tapes are attached to the resist edge before etching for easy handling. After the transfer, the samples are dried at ambient temperature. Post-baking (160 °C) is performed to remove residual H<sub>2</sub>O. Finally, the copolymer is removed either by hot acetone or by baking samples at 450 °C in H<sub>2</sub> and Ar during 10 min.

Fig. 4(a) and (b) shows optical micrographs of the graphene transferred from the Cu foil and thin film, respectively, onto 300-nm-thick SiO<sub>2</sub> thermally grown on Si wafers; this thickness gives maximum optical contrast. Bi-layer or multilayer graphene flakes (as in [10]–[13]) are not observed, suggesting that the graphene is highly uniform over large areas. In Fig. 4(a), the (almost) vertical lines are replicas of the microstructure at the unpolished surface of the mechanically processed Cu foils [see Fig. 3(a)]. The graphene transferred from Cu thin films is smoother. Shown in Fig. 4(b) image is intentionally captured at the place with a few wrinkles in the graphene and some uncovered areas of the substrate to guide the eye and visually set off the graphene layer.

The large-area graphene can be suspended by the transfer to transmission electron microscopy (TEM) grids. Fig. 5(a) demonstrates that a  $\sim\phi 12\ \mu\text{m}$  hole in the *a*-C network within a Cu TEM grid is completely covered by graphene with no visible ruptures and other defects. This indicates excellent mechanical properties of the graphene. Some wrinkles yet present at several places allow for the direct counting of graphene layers [see Fig. 5(b)]. It is seen that the as-produced graphene is a monolayer in this case.

Further proof can be found in Fig. 6 where two Raman spectra are shown, corresponding to graphene deposited on the Cu foil or thin film, and subsequently transferred to SiO<sub>2</sub>/Si. In both cases,

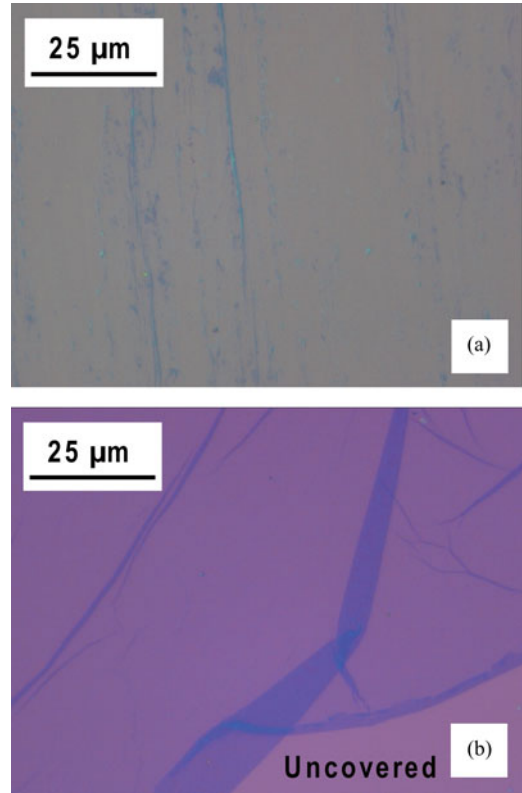


Fig. 4. Optical micrographs of the graphene produced by (a) Cu foils and (b) Cu thin films after being transferred to 300 nm SiO<sub>2</sub>/Si.

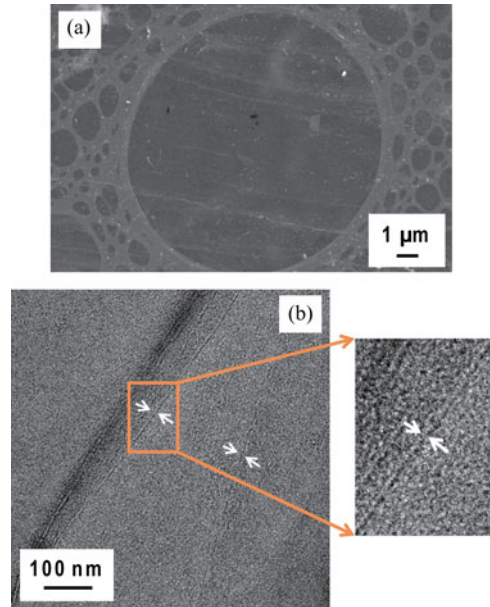


Fig. 5. (a) SEM image of the Cu-foil-grown graphene transferred to a standard TEM carbon grid. (b) Plan-view TEM image of the graphene. The enlarged micrograph indicates that the film is monolayer.

the G band and 2-D band peaks at  $\sim 1594\ \text{cm}^{-1}$  and  $\sim 2690\ \text{cm}^{-1}$  are clearly observed. The G and 2-D bands are signatures of *sp*<sup>2</sup> hybridized carbon [23]. In Fig. 6, the 2-D peak intensity is much higher than the G peak intensity. The 2-D peak is symmetric



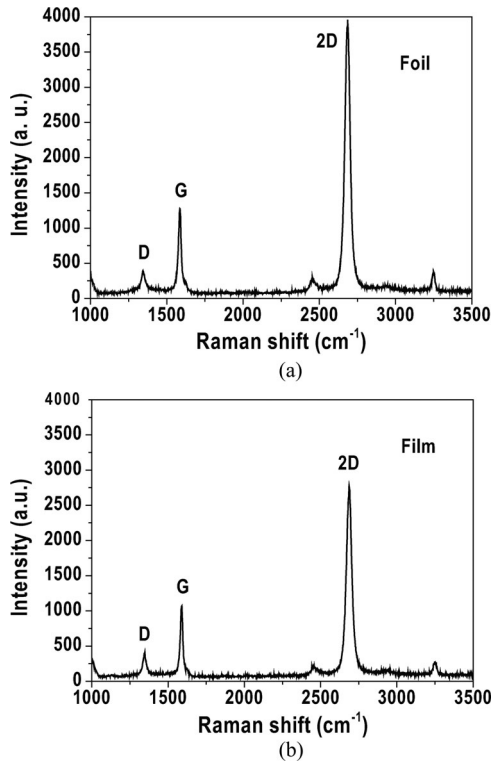


Fig. 6. Representative Raman spectra (514 nm) of the graphene grown on (a) Cu foils and (b) Cu thin films, and subsequently transferred to  $\text{SiO}_2/\text{Si}$ .

with a full width at half maximum (FWHM) of  $\sim 37 \text{ cm}^{-1}$  for (a) and  $\sim 42 \text{ cm}^{-1}$  for (b), which is typical of CVD graphene [8], but larger than that of exfoliated graphene ( $28\text{--}30 \text{ cm}^{-1}$ ) [24]. A small D peak at  $\sim 1345 \text{ cm}^{-1}$  is detected, implying a number of defects in the carbon lattice. The 2G band [23] and the  $2450 \text{ cm}^{-1}$  band [25] are attributed to high-order Raman signals.

#### IV. DEVICE CHARACTERIZATION

The Hall-bar structures are fabricated by electron beam lithography (EBL) or, in some cases, conventional photolithography. First, the Hall-bar patterns are made by EBL using ZEP 520A resist and oxygen plasma etching. Then, the electrodes connecting to the graphene structure are defined by the lift-off method using the double-layer resist MMA(8.5)MAA (EL10)/PMMA 950A2 and 3 nm Ti and 77 nm Au thin films deposited by electron beam evaporation.

Electron transport measurements are carried out in air (room temperature) and liquid helium (4.2 K) without prior sample annealing. Typically, the transport characteristics of the as-grown graphene is ohmic; the sheet resistance  $R_s$  of  $\sim 0.6 \text{ k}\Omega/\square$  ( $V_g = 0 \text{ V}$ ) at room temperature is suitable for applications like transparent conducting electrodes. An optical micrograph of a device made of graphene transferred from Cu foil is displayed in Fig. 7(a) with S and D denoting the source and drain electrodes, respectively. There are four more (potential) electrodes in this device allowing for Hall-effect- and four-probe resistance measurements. The active area of this device enclosed between the four probes is  $2 \times 4 \mu\text{m}^2$  (two squares). Fig. 7(b) shows

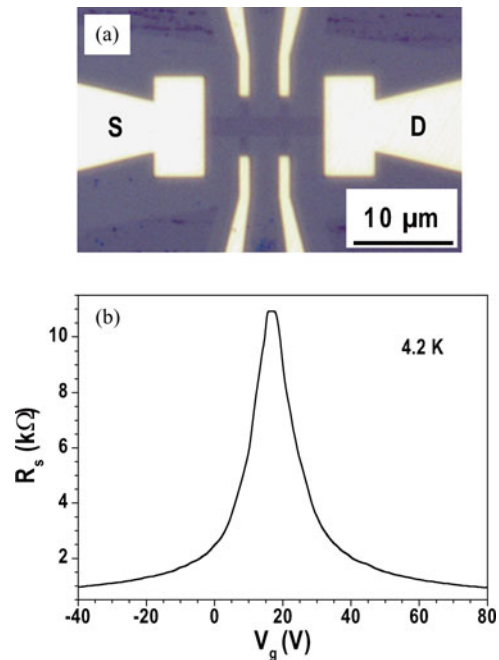


Fig. 7. (a) An optical image of a Hall device fabricated by EBL. The current-bias source and drain contacts are denoted by S and D, respectively. The other four electrodes allow for four-probe resistance measurements. (b) A typical  $R_s - V_g$  curve of the device shown in (a), measured with the four-probe configuration at liquid-helium temperature. The symmetric bell-shape of the curve suggests a nearly equal mobility of the electrons and holes.

the electrical-field effect,  $R_s(V_g)$ , in the graphene device [Fig. 7(a)] measured at 4.2 K. The gate voltage  $V_g$  is applied to the doped Si substrate relative to the graphene structure—a back-gate configuration. Calculated from the slope of the symmetric bell-shaped  $R_s(V_g)$  curve, the mobilities for electrons and holes are both  $\sim 1800 \text{ cm}^2/\text{Vs}$  while decreasing to  $\mu_e \approx \mu_h \approx 1200 \text{ cm}^2/\text{Vs}$  at room temperature. The former value is confirmed by the Hall-effect measurements (data not shown) at 4.2 K. The Dirac point of graphene is at  $\sim 16.5 \text{ V}$ , implying a minor charge doping. On the other hand, devices made by photolithography generally show a pronounced  $p$ -doping, presumably due to the residues of Shipley S1813 resist at the surface of graphene [26]. Finally, we note that some of our devices show carrier mobilities up to  $\sim 2700 \text{ cm}^2/\text{Vs}$  at room temperature, calculated by a two-terminal device model [27]. Nevertheless, both the transport measurements and the Raman spectra suggest that further improvements, such as a more accurate temperature control during the CVD and a more reproducible transfer process, are necessary to enhance the quality of graphene to a level which is comparable to the mechanically exfoliated graphene.

#### V. CONCLUSION

In summary, we have carried out a systematic study of very low partial pressure CVD of graphene using Cu foils or thin films as catalysts. As-synthesized large-area monolayer graphene is uniform, with no bilayer or multilayer parts detected. We use diluted  $\text{HNO}_3$  to etch copper and metal impurities, giving a cleaner graphene surface compared to the case of ferric saline as

etchant. Adequate mechanical, optical, and electrical properties of the graphene are revealed by SEM, TEM, Raman spectra, and transport measurements. It is hoped that the results presented here will aid in understanding of the Cu-catalyzed graphene CVD process under these extreme process conditions.

#### ACKNOWLEDGMENT

The authors thank Dr. Johannes Svensson from Lund University, Sweden, for his kind help.

#### REFERENCES

- [1] R. V. Noorden, "The trials of new carbon," *Nature*, vol. 469, pp. 14–16, 2011.
- [2] X. Li, X. Wang, L. Zhang, S. Lee, and H. Dai, "Chemically derived, ultrasmooth graphene nanoribbon semiconductors," *Science*, vol. 319, pp. 1229–1232, 2008.
- [3] H. Wang, D. Nezhich, J. Kong, and T. Palacios, "Graphene frequency multipliers," *IEEE Electron Device Lett.*, vol. 30, pp. 547–549, 2009.
- [4] F. Schwierz, "Graphene transistors," *Nature Nanotechnol.*, vol. 5, pp. 487–496, 2010.
- [5] K. S. Novoselov, A. K. Geim, S. V. Morozov, D. Jiang, Y. Zhang, S. V. Dubonos, I. V. Grigorieva, and A. A. Firsov, "Electric field effect in atomically thin carbon films," *Science*, vol. 306, pp. 666–669, 2004.
- [6] J. Kedzierski, P. Hsu, P. Healey, P. W. Wyatt, C. L. Keast, M. Sprinkle, C. Berger, and W. A. de Heer, "Epitaxial graphene transistors on SiC substrates," *IEEE Trans. Electron Devices*, vol. 55, pp. 2078–2085, 2008.
- [7] L. Gomez De Arco, Y. Zhang, A. Kumar, and C. Zhou, "Synthesis, transfer, and devices of single- and few-layer graphene by chemical vapor deposition," *IEEE Trans. Nanotechnol.*, vol. 8, pp. 135–138, 2009.
- [8] X. Li, W. Cai, J. An, S. Kim, J. Nah, D. Yang, R. Piner, A. Velamakanni, I. Jung, E. Tutuc, S. K. Banerjee, L. Colombo, and R. S. Ruoff, "Large-area synthesis of high-quality and uniform graphene films on copper foils," *Science*, vol. 324, pp. 1312–1314, 2009.
- [9] S. Bae, H. Kim, Y. Lee, X. Xu, J.-S. Park, Y. Zheng, J. Balakrishnan, T. Lei, H. R. Kim, Y. I. Song, Y.-J. Kim, K. S. Kim, B. Ozyilmaz, J.-H. Ahn, B. H. Hong, and S. Iijima, "Roll-to-roll production of 30-inch graphene films for transparent electrodes," *Nature Nanotechnol.*, vol. 5, pp. 574–578, 2010.
- [10] S. Bhaviripudi, X. Jia, M. S. Dresselhaus, and J. Kong, "Role of kinetic factors in chemical vapor deposition synthesis of uniform large area graphene using copper catalyst," *Nano Lett.*, vol. 10, pp. 4128–4133, 2010.
- [11] W. Cai, Y. Zhu, X. Li, R. D. Piner, and R. S. Ruoff, "Large area few-layer graphene/graphite films as transparent thin conducting electrodes," *Appl. Phys. Lett.*, vol. 95, p. 123115, 2009.
- [12] L. Gao, W. Ren, J. Zhao, L.-P. Ma, Z. Chen, and H.-M. Cheng, "Efficient growth of high-quality graphene films on Cu foils by ambient pressure chemical vapor deposition," *Appl. Phys. Lett.*, vol. 97, p. 183109, 2010.
- [13] S. Lee, K. Lee, and Z. Zhong, "Wafer scale homogeneous bilayer graphene films by chemical vapor deposition," *Nano Lett.*, vol. 10, pp. 4702–4707, 2010.
- [14] J. Sun, P. Jin, and Z.-G. Wang, "Extremely low density InAs quantum dots realized in situ on (100) GaAs," *Nanotechnology*, vol. 15, pp. 1763–1766, 2004.
- [15] J. Sun, R. Li, C. Zhao, L. Yu, X. Ye, B. Xu, Y. Chen, and Z. Wang, "Room-temperature observation of electron resonant tunneling through InAs/AlAs quantum dots," *Electrochem. Solid State Lett.*, vol. 9, pp. G167–G170, 2006.
- [16] A.-S. Johansson and J.-O. Carlsson, "Crystallinity, morphology and microstructure of chemical-vapor-deposited carbon films on different substrates," *Thin Solid Films*, vol. 261, pp. 52–58, 1995.
- [17] A. Ismach, C. Druzgalski, S. Penwell, M. Zheng, A. Javey, J. Bokor, and Y. Zhang, "Direct chemical vapor deposition of graphene on dielectric surfaces," *Nano Lett.*, vol. 10, pp. 1542–1548, 2010.
- [18] G. I. Kozlov and V. G. Knorre, "Single-pulse shock tube studies on the kinetics of the thermal decomposition of methane," *Combustion Flame*, vol. 6, pp. 253–263, 1962.
- [19] H. Cao, Q. Yu, L. A. Jauregui, J. Tian, W. Wu, Z. Liu, R. Jalilian, D. K. Benjamin, Z. Jiang, J. Bao, S. S. S. Pei, and Y. P. Chen, "Wafer-scale graphene synthesized by chemical vapor deposition at ambient pressure," *arXiv:0910.4329v1*, unpublished.

- [20] H. Cao, Q. Yu, L. A. Jauregui, J. Tian, W. Wu, Z. Liu, R. Jalilian, D. K. Benjamin, Z. Jiang, J. Bao, S. S. Pei, and Y. P. Chen, "Electronic transport in chemical vapor deposited graphene synthesized on Cu: Quantum Hall effect and weak localization," *Appl. Phys. Lett.*, vol. 96, p. 122106, 2010.
- [21] Y. Lee, S. Bae, H. Jang, S. Jang, S.-E. Zhu, S. H. Sim, Y. I. Song, B. H. Hong, and J.-H. Ahn, "Wafer-scale synthesis and transfer of graphene films," *Nano Lett.*, vol. 10, pp. 490–493, 2010.
- [22] X. Li, C. W. Magnuson, A. Venugopal, J. An, J. W. Suk, B. Han, M. Borysiak, W. Cai, A. Velamakanni, Y. Zhu, L. Fu, E. M. Vogel, E. Voelkl, L. Colombo, and R. S. Ruoff, "Graphene films with large domain size by a two-step chemical vapor deposition process," *Nano Lett.*, vol. 10, pp. 4328–4334, 2010.
- [23] M. S. Dresselhaus, A. Jorio, M. Hofmann, G. Dresselhaus, and R. Saito, "Perspectives on carbon nanotubes and graphene Raman spectroscopy," *Nano Lett.*, vol. 10, pp. 751–758, 2010.
- [24] A. C. Ferrari, J. C. Meyer, V. Scardaci, C. Casiraghi, M. Lazzeri, F. Mauri, S. Piscanec, D. Jiang, K. S. Novoselov, S. Roth, and A. K. Geim, "Raman spectrum of graphene and graphene layers," *Phys. Rev. Lett.*, vol. 97, p. 187401, 2006.
- [25] T. Shimada, T. Sugai, C. Fantini, M. Souza, L. G. Cancado, A. Jorio, M. A. Pimenta, R. Saito, A. Gruneis, G. Dresselhaus, M. S. Dresselhaus, Y. Ohno, T. Mizutani, and H. Shinohara, "Origin of the 2450  $\text{cm}^{-1}$  Raman bands in HOPG, single-wall and double-wall carbon nanotubes," *Carbon*, vol. 43, pp. 1049–1054, 2005.
- [26] J. Sun, N. Lindvall, M. T. Cole, K. B. K. Teo, and A. Yurgens, "Large-area uniform graphene-like thin films grown by chemical vapor deposition directly on silicon nitride," *Appl. Phys. Lett.*, vol. 98, p. 252107, 2011.
- [27] S. Kim, J. Nah, I. Jo, D. Shahrjerdi, L. Colombo, Z. Yao, E. Tutuc, and S. K. Banerjee, "Realization of a high mobility dual-gated graphene field-effect transistor with  $\text{Al}_2\text{O}_3$  dielectric," *Appl. Phys. Lett.*, vol. 94, p. 062107, 2009.



**Jie Sun** was born in 1977. He holds two Ph.D. degrees from the Institute of Semiconductors in Chinese Academy of Sciences (received in 2007) and Solid State Physics Division in Lund University, Sweden (received in 2009). His major is semiconductor materials and devices, particularly in low-dimensional structure growth and nanodevice fabrication/characterization.

He is specialized in GaAs, InP- and Si-based semiconductors, and oxide-based dielectrics ( $\text{SiO}_2$ ,  $\text{Al}_2\text{O}_3$  and  $\text{HfO}_2$ ). He has designed and studied ballistic and quantum devices, as well as analogue and digital nanoelectronic circuitry. He has published more than 40 papers with a Hirsch-index of 10. Currently, he is working in Chalmers University of Technology, Gothenburg, Sweden, as a Postdoctoral Research Fellow in carbon nanoelectronics, focusing on CVD graphene and functional devices based on such materials. More details about Dr. Sun can be found at <http://blog.sciencenet.cn/u/albertjefferson>.



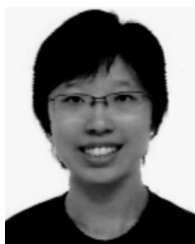
**Niclas Lindvall** received the B.S. degree in engineering physics and M.S. degree in nanoscale science and technology, in 2010, from Chalmers University of Technology, Göteborg, Sweden. He is currently working toward the Ph.D. degree in the Quantum Device Physics group at the Department of Microtechnology and Nanoscience at Chalmers.

His current research interest includes the fabrication and characterization of graphene as well as graphene-based superconducting devices.



**Matthew T. Cole** received the M.Eng. (Hons.) degree in engineering sciences from the University of Oxford, U.K., in 2008. After spending time as a Research Associate in the D. Ham group, School of Engineering and Applied Sciences, at Harvard University, USA, he has, since 2008, been working toward the Ph.D. degree in the Department of Electrical Engineering, Cambridge University, U.K.

His current research interests include the areas of discrete nanomaterials and nanoelectronics, focusing on the chemical vapor deposition of carbon nanotubes and graphene and their development and integration into new electronic devices. His research also focuses on the alignment of carbon nanotubes and transfer of carbon nanotubes and graphene onto flexible transparent polycarbonate supports. He is member of the Institute of Engineering and Technology.



**Koh T. T. Angel** received the bachelor's degree in materials science from the National University of Singapore (NUS), Singapore, in 2008. She is currently pursuing the Ph.D. degree in the Department of Materials Science and Engineering, NUS.

Her research interests include the synthesis of carbon nanostructured films and graphene together with the study of their electron emission and transport properties.



**Teng Wang** (S'06) received the B.Eng. degree in automation from Tongji University, China, and M.Sc degree in microsystem integration technology from Chalmers University of Technology, Sweden. He is currently working toward the Ph.D. degree in Bio-Nano Systems Laboratory, at the department of Microtechnology and Nanoscience of Chalmers.

His main research interest and activity are on the development of carbon nanotube-based off-chip interconnects for densely integrated electronics.



**Kenneth B. K. Teo** (M'96) received the B.E. degree in electrical and electronic engineering from the University of Canterbury (NZ), in 1996. He received the Ph.D. degree, in 2002, at the Electrical Engineering Department of University of Cambridge, U.K.

He worked as a Product Development Engineer for PDL Holdings, then as a Project Engineer for Defence Material Organisation. His research focused on the growth and application of carbon nanotubes and graphene. He joined AIXTRON in 2007 as the Director of Nanoinstruments and is responsible for its carbon nanomaterials equipment division. He held Royal Academy of Engineering Research Fellowship and Lectureship positions at the Electrical Engineering Department of University of Cambridge, U.K.



**Daniel H. C. Chua** received the B.Sc. (Hons.) in physics, in 1998, from National University of Singapore, Singapore, and the Ph.D. degree in electrical engineering, in 2005, from Cambridge University, U.K.

He was appointed as Postdoctoral Junior Research Fellow of Clare Hall, Cambridge University, and concurrently a Research Associate in the Engineering Department. Prior to his arrival in Cambridge, he had worked in semiconductor and hard disk media industry as an Applications Engineer in failure analysis and materials characterization for three years. Currently, he is an Assistant Professor in the Department of Materials Science and Engineering in the National University of Singapore, Singapore.



**Johan Liu** (F'06) received the master's and Ph.D. degrees in materials science from the Royal Institute of Technology, Sweden.

He is currently Full Professor in electronics production and Head of Bio Nano Systems Laboratory, Department of Microtechnology and Nanoscience in Chalmers University of Technology, Sweden. He is also director of SMIT Center both at Chalmers University of Technology and Shanghai University, China (a joint center between Sweden and China).

Before joining Chalmers University of Technology, he served in various positions at the Swedish Institute for Production Research (IVF) as Project Manager, Group Leader and Division Manager. His research field covers mainly nanopackaging materials and process for electronics and MEMS, including 3-D CNT TSV technology, CNT Cooling technology, CNT Bumping, high temperature stable conductive adhesives, nanosoldering, nanothermal interface materials, and nanomaterials for thermoelectrical applications and nanoscaffolds for biomedical applications. He has published 2 books, 350 papers in journals, proceedings, and 14 book chapters with a Hirsch index of 19 and with a citation of over 1096 times. He has 20 patents accepted or filed and has given about 35 key note/invited talks during the last 20 years.

Prof. Liu has received many awards including IEEE Exceptional Technical Achievement Award and IEEE CPMT Transaction Best Paper Award in "Advanced Packaging." He is a member of the Royal Swedish Academy of Engineering Sciences.



**August Yurgens** received the B.S. degree in physics and electronics, in 1982, from Moscow Institute of Physics and Technology, Moscow, Russia, and the Ph.D. degree in physics and mathematics in 1987, from Kapitza Institute, Moscow, Russia.

Currently, he is a Professor at the Department of Microtechnology and Nanoscience of Chalmers University of Technology, Gothenburg, Sweden. His research interests include the fundamental properties of low-dimensional and strongly-correlated electronic systems. In particular, he is active in experimental studies on layered high-temperature superconductors (HTS) and graphene. The former area also includes practical applications like THz-range emitters based on the so called intrinsic Josephson junctions naturally occurring in some of the HTS.