

Future integration of Symbolic Computation and Satisfiability Checking

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Satisfiability Checking — SAT

Given a formula \mathcal{P} in propositional logic, normally in Conjunctive Normal Form (CNF)

$$(l_{1,1} \vee l_{1,2} \vee \dots) \wedge (l_{2,1} \vee l_{2,2} \vee \dots) \wedge \dots$$

where the $l_{i,j}$ are either p_k or $\neg p_k$ for a set of propositional variables $\{p_k\}$, either find a satisfying assignment of true/false to the p_k , or state (correctly!) that no such exists.

Theory 3-SAT is NP-complete

Practice “real-world” examples with millions of clauses are solved in competitions, and it’s hard to produce hard examples [Spe15]

Eclipse uses a SAT solver to resolve package dependencies

BMW etc. use SAT solvers to configure cars for customers
[KS00]: >2M (unknowing) SAT users/year

Many tricks especially lemma generation

Instead of the $\{p_k\}$ being free Booleans, they are elements of some underlying theory (for us, polynomials over \mathbf{R} with $=$, $>$ etc.) with underlying variables x_i , and it is the x_i whose values we seek.

So $p_1 \wedge p_2$ is solvable for free Boolean p_k , but not when $p_1 \equiv x < 0$ and $p_2 \equiv x > 1$.

When $p_1 \equiv x > 0$ and $p_2 \equiv x < 1$ it is solvable with, say, $x = 1/2$.

Quantifiers?

Showing satisfiability of \mathcal{P} is answering (positively)

$\exists x_1, \dots, x_n \mathcal{P}(x_1, \dots, x_n)$, and unsatisfiability is answering it negatively, or answering $\forall x_1, \dots, x_n \neg \mathcal{P}(x_1, \dots, x_n)$ positively.

In many verification applications, \mathcal{P} is “a dangerous state”, and we want to answer $\forall x_1, \dots, x_n \neg \mathcal{P}(x_1, \dots, x_n)$ positively, i.e. “the system cannot enter a dangerous state” *preferably with a proof*

One technique [JdM12] essentially drives the CAD algorithm backwards: looking for a counter-example

$\exists x_1 \dots \forall y_1 \dots \mathcal{P}(x_1, \dots, y_1, \dots)$ is considered in

[CAR⁺15, RDK⁺15], but seems to require more technology

This is “there exists a solution such that the system cannot enter a dangerous state” — “we haven’t painted ourselves into a corner”

Why do modern SAT-solvers do so well?

- A lot of attention to constant(ish) factors.
- Even more attention to $O(n)$ factors (watched literals)
- A great deal of accumulated heuristics
- Keep reformulating the problem (re-ordering the p_i) [HH10], e.g. every 100, 100,200,100,100,200,400,... [LSZ93] deductions we restart

But keeping track of “useful” (i.e. short) lemmas learned (which potentially invalidates the argument for [LSZ93])

Lemmas?

In principle, there's much more scope for lemma-learning in (polynomial) SMT than straight SAT: for example $x^2 + y^2 \leq 1 \Rightarrow (x \geq -1) \wedge (x \leq 1)$, but the only use currently made of this sort of reasoning in mainstream CAD is $(f = 0 \wedge g = 0) \Rightarrow \text{res}_{x_n}(f, g) = 0$ (extensions in [DE16])



- Q what is a “useful” lemma in this context?
- Q Is there an equivalent of “short”?
- Q Research shows utility (75+% of the time) of $(f = 0 \wedge g > 0) \Rightarrow (\hat{g} > 0)$ where \hat{g} is the Gröbner reduction of g by f

CAD is very dependent on the order of the x_k , with significant research, and effort at runtime, going into choosing the “best” order [DSS04, HEW⁺14]

On the other hand, SAT solvers frequently [HH10] reorder the p_i , received wisdom being that there is no *a priori* best order

- Q What is the relationship between the order of the p_i and the x_k ?

Questions?

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